



## Department of Electronics Engineering

Announces  
One Week STTP on

### “Machine Learning and Smart Devices”

July 02 to July 07, 2018



**Venue:** VIT, Wadala, Mumbai

### About VIT

**Vidyalankar Institute of Technology (VIT)** is an 'A' grade Engineering degree college approved by the AICTE, the DTE (Maharashtra), and affiliated to the University of Mumbai. Incepted in the year 1999, the Institute has steadily gained recognition and attracts top talent from across the country.

VIT conducts graduate courses in Electronics Engineering, Electronics and Telecommunication Engineering, Computer Engineering, Information Technology Engineering and Biomedical Engineering with an average annual intake of about 900 students. The Institute also conducts Masters in Engineering programs in Electronics and Telecommunication Engineering, Computer Engineering, and Information Technology Engineering in addition to Masters in Management Studies. Institute also offers doctoral program (Ph.D.) in Computer Engineering.

Our vision is to provide an educational environment based on ethics, discipline and knowledge. Three programs namely Electronics Engineering, Electronics and Telecommunication Engineering and Biomedical Engineering have received NBA accreditation status. The infrastructure at institute won the Design Share 2007 Award and it proposes an appealing sight which is learning environment friendly. We, at Vidyalankar Institute of Technology, aim to create industry-ready professionals, research scholars and proficient entrepreneurs by infusing the right blend of technological expertise, professional acumen, and social sensitivity in academics and sensitize them towards society.

We encourage innovative thinking and intellectually stimulate them towards technology and research by generating indigenous and appropriate laboratories and activities.

### About Department of Electronics Engineering

Department of Electronics Engineering, established in year 1999, is accredited by National Board of Accreditation (NBA), New Delhi for the period of 3 years from July 2016. The department has also received permanent affiliation from University from 2017-18. The overall UG intake of the department is 120 students for first year and 60 students for Direct Second Year.

The department has a clear vision to become a center of excellence in the varied domains of Electronics Engineering. It is rich in human resources with expertise in various domains. Well- equipped laboratories to cater domain specific experimentation and research. Electronics Engineering department at VIT has always rendered cumulative efforts to enhance learning by conducting various workshops, Value Added Courses, talks by experts, seminars and short term training programs.

### Objectives of STTP:

1. To provide an exposure to current trends and research in the fields of Machine Learning, Artificial Intelligence, Cloud Computing and Smart Devices.
2. To open up the opportunities to undertake the research in the above mentioned fields in addition to Data Sciences which are in demand currently.

### Who Can Apply:

This course is addressed to students, faculty members and research scholars of various disciplines in the field of engineering. Certificate will be given on successful completion.

**Registration Fee for STTP is INR 2200/-**

**Last Date of Registration: 23 June, 2018**

### **Workshop Contents:**

- Impact of Machine Learning in our lives(Keynote)
- Machine Learning in Speech Processing
- Concepts of Classification and Clustering
- Hands – On session using ORANGE platform.
- Practical approach towards Machine Learning Algorithms (Hands-on)
- Implementation of AI in industry
- IBM Cloud for Machine Learning (Hands On)
- Applications of Machine Learning in Video and Image Processing
- Smart Devices
- Video Analytics - ML and Smart Devices to help humans understand videos better and faster
- Applications of Machine Learning in Satellite and Image Processing
- Deep Learning
- Constraint Optimization in Support Vector Machine Classification with visit to COE

### **Prominent Speakers:**

- Dr. Sunil Kumar Kopparapu, Principal Scientist TCS Research and Innovation
- Dr. B K Mohan, Professor, CSRE, IIT Bombay
- Dr. Rajbabu Velmurugan, Associate Professor IIT Bombay
- Mr. Devesh Rajyadhyax, CEO, Founder, Cerelabs
- Mr. Nilesh Dalvi, Free Lance Consultant in Machine Learning
- Dr. Faruk Kazi, Professor and Dean Industrial Research Development and Consultant VJTI, Mumbai
- Dr. Jonathan Joshi, CEO, Eduvance
- Prof. Shrikant Velenkar, Professor VIT, Mumbai
- Dr. Varsha Turkar, Head Department of Information Technology, VIT, Mumbai

### **Schedule:**

**Duration:** July 02 to July 07, 2018

**Timings:** 10:00 a.m. to 5.00 p.m.

### **Patrons:**

Ms. Rashmi Deshpande (Chair Person, Vidyankar Dnyanapeeth Trust)  
Mr. Vishwas Deshpande (Managing Trustee, Vidyankar Dnyanapeeth Trust)  
Mr. Avinash Chatorikar (Secretary, Vidyankar Dnyanapeeth Trust)  
Mr. Milind Tadwalkar (Director, Vidyankar Dnyanapeeth Trust)  
Dr. S. A. Patekar (Principal, Vidyankar Institute of Technology)  
Prof. Varsha Bhosale (Vice Principal, Vidyankar Institute of Technology)

### **Conveners:**

Dr. Anjali Deshpande  
Prof. Shrikant Velenkar

### **Coordinators:**

Prof. Vivek P. Patkar – 9769565139 – vivek.patkar@vit.edu.in  
Prof. Akshatha Bhat – 8105051261 – akshata.bhat@vit.edu.in  
Prof. Sunil Kavatkar – 9960182054 – sunil.kavatkar@vit.edu.in

### **Organized by:**

Vidyankar Institute of Technology  
Vidyankar College Marg,  
Wadala (E), Mumbai- 400 037  
Telefax: +91 22 24161126  
Web: www.vit.edu.in

### **Registration Form Available At:**

<https://tinyurl.com/y9jzzjit>

### **Registration Form**

**Name:** \_\_\_\_\_

**Designation:** \_\_\_\_\_

**Department:** \_\_\_\_\_

**Qualification:** \_\_\_\_\_

**Contact Number:** \_\_\_\_\_

**E-Mail:** \_\_\_\_\_

**Date:** \_\_\_\_\_

**Place:** \_\_\_\_\_

**DD/Cheque Number:** \_\_\_\_\_

**Bank Name & Branch:** \_\_\_\_\_

\_\_\_\_\_  
**Signature of Applicant**

**Note:**

**Registration fees includes only Tea and Snacks. Good Quality food is available in the canteen. Participants can avail this facility at their own cost.**