

EXTRACTING



DEPARTMENT OF ELECTRONICS AND
TELECOMMUNICATION ENGINEERING

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Vision of the Department

To be recognized as a center of Excellence in the field of Electronics and Telecommunication Engineering where learners are nurtured in a scholarly environment to evolve into competent professionals to benefit society.

Mission of the Department

1. Evolve a curriculum which emphasizes on strong engineering fundamentals with the flexibility to choose advanced courses of interest and gain exposure to tools and techniques in Electronics and Telecommunication Engineering.
2. Encourage a teaching-learning process in which highly competent faculty share a symbiotic association with the institutes of repute.
3. Facilitate creation and dissemination of Electronics and Telecommunication knowledge through a digitally-enabled learning environment.
4. Develop academic and infrastructural facilities with modern equipment and other learning resources and encourage reciprocal sharing with other institutes through networking.
5. Establish a centre of excellence to enhance academia – Electronics and Telecommunication industry partnership and work on collaborative projects.

Programme Educational Objectives (PEO)

1. Graduates will be able to apply Electronics and Telecommunication knowledge to design technically sound systems, adopt new technologies through lifelong learning and to lead a successful career in diverse domains and streams.
2. Graduates will be able to show ability of Research and Development and Entrepreneurship skills.
3. Graduates will be able to exhibit professionalism, ethics, team work and social awareness in their career.

DR. ARPIT RAWANKAR

HEAD OF DEPARTMENT, ELECTRONICS AND TELECOMMUNICATION
ENGINEERING

Hello Everyone,

A warm welcome to all readers.

In this semester, we successfully conducted regular academic and extra curricular activity. I would like to appreciate efforts of my teaching and nonteaching staff in completing committee related activities properly. Also, department conducted various drives to engage students in completing certifications in Simulation Software domain to gain skills. Department also encouraged students to participate in sports, co curricular and extracurricular activities.

I wish my students a great success in upcoming End semester examinations.

WHAT'S INSIDE

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Prof.Vibha Wali
Prof.Dattatray Bade

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Pragnya Govindula

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Durwang Sawant

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Department Activity





“Patents need inventors more than inventors need patents”

— Kalyan C. Kankanala



ETSA in Collaboration with ISTE organized a Guest lecture on FPGA & ARM: Another approach to Embedded System

ETSA in collaboration with ISTE presents
FPGA & ARM: Another approach to Embedded System

Event Highlights:

- 1) FPGA [Field Programmable Gate Arrays]
 - Intel Altera and AMD Xilinx
 - A very demanding and growing field in Electronics
- 2) ARM Cortex series microcontroller/ microprocessor
- 3) Q&A session with the speaker
- 4) Certificate of participation

Mr. Rahul Pandey
System Validation Engineer
at Intel Co-operation

24th September 2022
5 pm onwards
Teams (Online platform)

Scan to register

DATE: September 24th 2022

Speakers: Mr. Rahul Pandey, System Validation Engineer, Intel Co-operation

Name of the Committee: ETSA and ISTE

Activity Title: Introduction to FPGA Tools and ARM Microcontroller

The objective of the event was to learn about the functioning, execution, and overall software of fpga. Also, the intention of the event was to educate students on the various advancements in the embedded systems industry. Our speaker for the event Mr. Rahul Pandey (Alumni from 2019 batch) made the students aware of various opportunities present in core industry. He also spoke on brief history of FPGA. He explained Arm Architecture to students and elaborated on skill sets required to work in this industry. He later explained scope of FPGA and challenges in this industry. He also threw light on significance of Digital signal processing and coding in the field of Embedded systems. He also guided students through various pathway's after completing BE.

The event concluded with Q & A session. The event was a huge success with the attendance of 90 Participants .

Department Staff



Prof. Vibha Wali
ASSISTANT PROFESSOR

EDUCATION QUALIFICATION:
ME Electronics

WORKING EXPERIENCE:
21+YEARS

AREA OF SPECIALIZATION:
Object oriented programming, Neural networks, Image processing, Database management

Prof. Dattatray Bade
ASSISTANT PROFESSOR

EDUCATION QUALIFICATION:
ME Electronics and Telecommunication

WORKING EXPERIENCE:
17 Years

AREA OF SPECIALIZATION:
Circuit theory and networks, Signals and Systems, Probability and Random Processes
Digital Electronics



“Tell me and I forget. Teach me and I remember. Involve me
and I learn.
- Benjamin Franklin”

Student's Speak

Pragnya Govindula SE Div B

In a very short yet worthwhile time here VIT, I've had the privilege to learn so many things, more prominently, confident decision makings. Along with academic lessons, VIT has surely taught me valuable life lessons, which I'll cherish for the rest of my years.

Even though my college had started in the online mode, it surely gave us an insight of how fun and interesting offline mode will be. VIT encouraged us to do extra curricular activities like online courses on Coursera and many other platforms, internships from Intershala and Acmegrade, hands on experience in workshops, and visiting educational sites. It has taught us to enjoy our life by arranging various events and celebrating festivals. By joining student council and committees it has made us learn soft skills and hard skills as well.

The college has moulded my personality and helped me gain a clear vision of my future. At last, I thank and appreciate the support provided by the staff and faculty members.



Students Achievements:

1. Our BE Students Priti Patankar, Abhiraj Raghunath, Nitee Dhuri have been selected to work in IITB on the Project topic 'GPS Spoofing using software defined radio and GNU radio software. They are working under Dr.Dhananjay Patel. Currently they are working in information and radio systems lab in electrical engineering department which are equipped with various SDRs like ETTUS research, high computing computers and RTL dongles.
2. Our students Burhan Khan, Aniket Ambore (Team leader) , Dhanish kadam and team from batch 2021 won Smart India Hackathon 2022 Competition. They worked on the problem statement: Conversational Demographics Data Update System.

Figure below shows the team members and Mentor Prof.Akshay Loke



3. Our BE Students Mihir Madkaikar, Kshitijia Pudari, Kinnari Bhalerao, Archit Dukhande, Balaji Durai Masanam participated in Smart India Hackathon 2022 under the guidance of Dr.Dhananjay Patel. They Worked on the problem statement ' Lack of sustainability in appointment of faculty in higher educational institutions and colleges'.

“ Security used to be an inconvenience sometimes, but now it's a necessity all the time.
-Martina Navratilova ”

IETE and ISTE Committee Conducted a Workshop on 'Dip-Trace PCB Designing Tool'

VIT Vidyalankar Institute of Technology Accredited A+ by NAAC

IETE-VIT presents... a collaboration with ISTE-VIT

Dip-Trace PCB Designing Workshop

PCB & CIRCUIT DESIGN COURSE CONTENTS
An introduction to basic electronic components. Circuit designing basics.

INTRODUCTION TO PCB DESIGNING
Intro to PCB types. An overview for the circuit board designing.

SCHEMATIC DESIGN
Making of PCB layouts with troubleshooting error techniques

DIP-TRACE (PCB DESIGNING SOFTWARE)
Creating Schematic and Layout on the software. Exporting GERBER files for production

PCB PRINTING & ETCHING
Introduction to Printing and Etching Process. Practical demonstration.

DRILLING & SOLDERING
Practical demonstration after the drilling and soldering.

Prof. Pratik Mhatre
Mentor for the Event

Event
17th, 24th September & 1st October
Morning Shift : 9am to 3pm

Prof. Amit Maurya (IETE-VIT Convenor)
Prof. Vijay Purohit (ISTE-VIT Convenor)

FOR MORE DETAILS CONTACT
IETE-VIT
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Udbhav Kamath (Vice- Chairperson) - +91 93242 49893

ISTE-VIT
Soham Pal (Chairperson) - +91 93260 49827
Freny Babaria (General Secretary) - +91 70452 70932

SCAN TO REGISTER

Poster Credits: Tanmay Gurav

DATE: 17th, 24th September and 1st October 2022

SPEAKER: Prof. Pratik Mhatre

Organizing Committee: ISTE and IETE.

Target Audience: SE, TE and BE students of the Vidyalankar Institute of Technology

Objective: The objective of the event was to make students aware of techniques in PCB Designing.

Takeaway: Attendees got insights on

- PCB Circuit design and course contents.
- Introduction to PCB Designing
- DIP-TRACE (PCB Designing Software)
- PCB printing and Etching
- Drilling and Soldering

Brief: The session started with a warm welcome to speaker Prof. Pratik Mhatre. Prof. Pratik Mhatre then briefed on all the points mentioned in the takeaway. The event continued with practical demonstration of PCB Designing software. Students were briefed on the challenges in PCB Designing.

The session then was followed by Question and Answers round. The total strength for the session was 105.



Durwang Sawant
Batch 2015
Currently working as 'Deputy Vice President' in
Switch my loan company

Alumni Speak:

" I would like to take this opportunity and extend my gratitude towards my alma mater VIT. I would like to thank all my professors and support staff for all that you have done to make me what I am today. I am able to have a successful life because of your help and belief in me. Again, thank you for all that you have done to lead me to success. Your encouragement and faith in me doesn't go unnoticed".

THE EDITORIAL TEAM

PROF. AMEY S. REVANDKAR