



Vision of the Department

To be recognized as a center of Excellence in the field of Electronics and Telecommunication Engineering where learners are nurtured in a scholarly environment to evolve into competent professionals to benefit society.

Mission of the Department

1. Evolve a curriculum which emphasizes on strong engineering fundamentals with the flexibility to choose advanced courses of interest and gain exposure to tools and techniques in Electronics and Telecommunication Engineering.
2. Encourage a teaching-learning process in which highly competent faculty share a symbiotic association with the institutes of repute.
3. Facilitate creation and dissemination of Electronics and Telecommunication knowledge through a digitally-enabled learning environment.
4. Develop academic and infrastructural facilities with modern equipment and other learning resources and encourage reciprocal sharing with other institutes through networking.
5. Establish a centre of excellence to enhance academia – Electronics and Telecommunication industry partnership and work on collaborative projects.

Programme Educational Objectives (PEO)

1. Graduates will be able to apply Electronics and Telecommunication knowledge to design technically sound systems, adopt new technologies through lifelong learning and to lead a successful career in diverse domains and streams.
2. Graduates will be able to show ability of Research and Development and Entrepreneurship skills.
3. Graduates will be able to exhibit professionalism, ethics, teamwork and social awareness in their career.

DR. VARSHA TURKAR

HEAD OF DEPARTMENT, ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Hello Everyone.

A Warm welcome to all reader's

This Quarter started with a warm welcome to our SE, TE and BE students. As a regular practice, students were given challenging academic assignments, mentoring sessions and a strong inspiration to participate in various Hackathon competitions to build a strong technical base. In this Quarter, Our Department initiated inauguration of IEEE-GRSS Chapter. Also, department incorporated NEP(National Education Policy)-2020 in the curriculum. I am glad to mention that we also had organized a two days hands-on workshop on India's indigenous VEGA Processor and Ecosystem in collaboration with CDAC.

Furthermore, I would like to wish all my students a very good Luck for upcoming Examinations!!! Also best wishes for teaching and Non teaching staff.

WHAT'S INSIDE

Guest Lectures

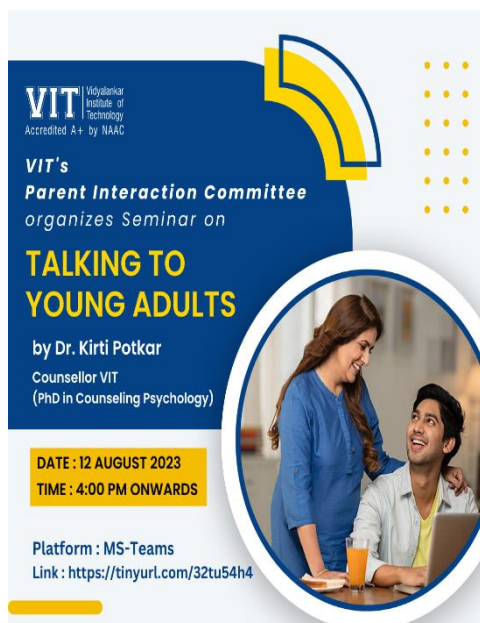
Alumni Talk
Prasanna Mandapalli

Events organised by the
Department

Faculty
Achievements/Initiatives



Guest Lecture Sessions



Date: 12th August 2023

Name of the Committee: Parent Interaction Committee

Activity Title: Seminar on 'Talking to young adults'

The seminar titled 'Talking to Adults' was organized by PIC for parents and students. The speaker for the event was Dr.Kirti Potkar who is PhD in counselling Psychology. The event was conducted on teams platform by Prof.Beena Ballal.

Dr.Kirti Potkar started the seminar by interacting with audience through her various questions which set the stage for further communication. Dr.Kirti covered and interacted with attendees on topic like Empathy, successful parent child relationship, building bridges in relationships, spending quality time with children.

Towards the end of the seminar Dr.Kirti answered all the queries of the attendees.



Date: 22nd September 2023

Speaker: Dr.Varsha Turkar

Activity Title: Fundamentals of Image Processing

Organizer:EXTC Department

Venue: M 201

Description : Dr.Varsha Turkar started the event by welcoming all participants. She gave general overview of the field of Image Processing and explained concepts related to basics and types of signal processing, difference between photographs ,images etc.

Dr.Varsha also shed light on the various uses and application of image processing. In the later part of the session students started working on MATLAB. Students learned about various keywords, functions, programming language syntax used for image processing in MATLAB.

Total 16 Participants attended the event.

“Tell me and I forget. Teach me and I remember. Involve me and I learn.”
 - Benjamin Franklin

Alumni Speak

Prasanna Mandapalli
 Associate Consultant Capgemini
 Batch 2020



I feel privileged and honoured to express my views for Vidyalankar Institute of Technology. I remember the time when I stepped in VIT as a FE student. VIT made me feel comfortable, positive and removed all my teenage fears, anxiety about future. There is lot to mention about my institute. I would like to describe many aspects about VIT. Firstly, its state-of-the-art infrastructure makes everyone feel comfortable and joyful. VIT has highly qualified faculty members, Higher end labs, Hi-Tech classrooms, clean sports and washroom facilities. The teaching methodologies which is imparted in the institute is unique and it elevates student's academic abilities to excel in studies. VIT has a very strong Teaching learning environment. The council activities in the Institute help students groom in extra-curricular activities.

I thank VIT for all the support, affection, Guidance it provided to us because of which we can stand firmly in corporate world.

Events Organized under IETE-VIT and With IEEE Bombay section

Under the guidance of Dr.Varsha Turkar and Prof.Pratik Mhatre, Department of EXTC in association with IETE and IEEE had organized above events

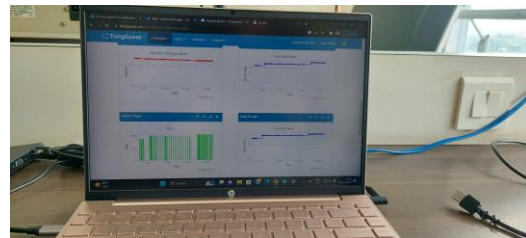
Department of EXTC organized 2 Days Hands on workshop on India's Indigenous VEGA Processors and Ecosystem.

About the Workshop: This workshop was national level event on the newly launched indigenous RISC-V based VEGA Processors organized on 15th and 16th September 2023. Experts from C-DAC, who are part of VEGA Processor development team, were the resource persons. The DIR-V workshop provided an insight into the VEGA Sereis of processors and ecosystem focussing on both the theoretical and practical aspects of the VEGA Processors, development boards, SDK and on building applications. Target Audience for workshop were students/Faculty from any branch of Engineering.

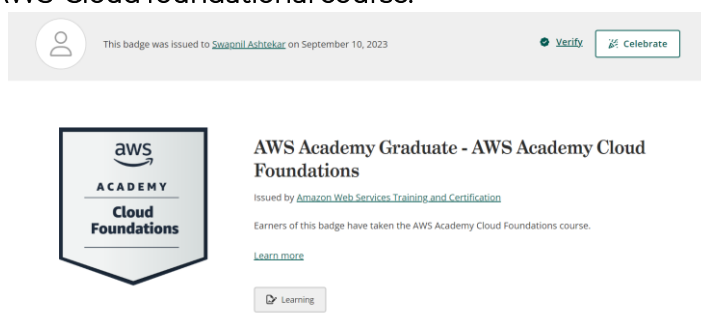
“ Education is the passport to the future, for tomorrow belongs to those who prepare for it today. —Malcolm X ”

Faculty Achievements/Initiatives/Value addition

- Prof.Vaibhav Kshirsagar took following Initiative that touches all aspects from basic electrochemistry to embedded systems/IOT and Data Science
1. A Quest for 'Determination of Surfactant (Monomer) Performance Efficacy' was perceived by chemical manufacturer's based in TTC Mahape MIDC Navi Mumbai. The Objective was to prototype a low cost, portable chemical probe to estimate performance of solid surfactant's in fabric cleaning operation. Conductometric investigation of Surfactant Performance Modelling for its efficiency is considered as one of the common practices in Chemical Processes and Analysis for Surfactant Manufacturers. Our final year students under my supervision have successfully developed a low cost Laboratory Chemical Probe device to accurately measure the "Critical Micelle Concentration" (CMC) parameter of the Solid Surfactant using its electrical properties. The probe was developed with TDS and temperature sensors and the device is powered up by Atmega Micrcontroller with addition of ESP (IoT) device to further enhance its capability to wirelessly store the captured data to the cloud systems. The device is able to record 180 to 200 samples of Surfactant solution per hour. Our student project group is now working further to analyse this data for its advanced modelling and feature extraction using machine learning approach. We are under technical assistance and establishment with **Galaxy Surfactants Pvt Ltd** Navi Mumbai, **Alconox International** and **ICT Mumbai** for this activity. Images below represent experimental set up for the ongoing work:



- Prof.Swapnil Ashtekar received course completion certificate and Digital badge from AWS-Cloud foundational course.



- Dr,Varsha Turkar received grant of INR 2,75000/- to attend and present paper at IGARSS 2023, a flagship conference of IEEE GRSS held at Paradema, California, USA in July 2023.

THE

EDITORIAL TEAM

PROF.AMEY S.REVANDKAR
Mithila Thakur(Student Co-
Ordinator TE EXTC)