



# Institution of Electronics & Telecommunication Engineers (IETE) & Vidyalankar Consultancy Services

## Joint Certified Skill Development Courses

Sr.	Course Code	Course Name	Location	Duration	Start Date	Sessions on	Timings	Fees in ₹
6	4533841	Linux & Linux Administration	VIT	35 Hrs.	21/09/2019	Sept. 21, 22, 28, 29 Oct. 2, 5 & 6	09.00 - 15.00	5,500/=
7	4523841	CAD PCB Designing with EAGLE and PCB Fabrication (with take home Circuit)	VIT	16 Hrs.	21/09/2019	Sept. 21, 22, 28, 29	09.00 – 13.00	1,900/=

### Highlights of the Courses –

- Course open to VIT / VP / VSIT and External Students
- Intensive & continuous Laboratory Hands-on Training
- Dual Certification by VIT and IETE/VCS
- On-line Examination and assessment
- Take-home kit with Pen-drive & Hand-out booklet
- Select participants get Industry Internship / Industry live project in Winter Break with Internship letter on completion (Tentative period: Dec.10 to Dec. 31, 2019)

<b>Course Code</b>	4533841	
<b>Course Fees</b>	₹ 5500	
<b>Course Name</b>	Linux and Linux Administration	
<b>Domain</b>	Operating Systems	
<b>No. of Hours</b>	40	
<b>Prerequisite</b>	Basic Computer Knowledge	
<b>Eligibility</b>	<ul style="list-style-type: none"> <li>• SE/TE/BE Degree Engineering Students</li> <li>• SYBSc(IT/COMPS)/TYBSc(IT/COMPS) Science Students</li> <li>• SY/TY Diploma Engineering Students</li> </ul>	
<b>Module</b>	<b>Contents</b>	<b>Hours</b>
Module-1	<b>Fundamentals of Linux</b> - Why Linux? What are the opportunities in linux? Who uses Linux? Boot loader b. Important files that are accessed during boot time c. Modifying the system boot behaviour	04
Module-2	<b>Linux Filesystem</b> - File system details e. Importance of Individual directories in the root(/), Linux Kernel, Initrd, Absolute paths and relative paths, File system permissions, File system related commands	06
Module-3	<b>Linux Server Installation</b> - Installation planning considerations, Installation hard disk planning, Software planning, Actual installation	06
Module-4	<b>Linux User Management</b> - Adding users b. Removing users, adding groups, Removing groups, Making users members of various groups, Modifying users, User permissions, User password setting and resetting, Sharing files between users	06
Module-5	<b>Linux Server services</b> a. General service concepts, commands, FTP, FTP service directories, Starting and stopping, Granting, denying access to ftp service, Public FTP shares, HTTP, Configuring HTTP service, HTTP service directories, Telnet configuration, Configuring the telnet service, xinetd service, XRDP service, MySQL / MariaDB service, Configuring the MySQL service, NFS Service	12
Module-6	<b>Linux Package Installation &amp; Scripting</b> - Installing new Package, Yum, compiling of new software to be installed, Updating the Linux server system, Shell scripting, For & While Loops	04
Module-7	<b>Linux Server Connections &amp; Backups</b> - Connecting to the Linux server, using ssh, telnet for server connections, Backups, backup details	02

<b>Course Code</b>	4523841	
<b>Course Fees</b>	₹ 1900	
<b>Course Name</b>	PCB Designing and Fabrication	
<b>Domain</b>	Electronics	
<b>No. of Hours</b>	16	
<b>Prerequisite</b>	Basic Computer Knowledge	
<b>Eligibility</b>	<ul style="list-style-type: none"> <li>• SE/TE/BE Degree Engineering Students</li> <li>• SYBSc(IT/COMPS)/TYBSc(IT/COMPS) Science Students</li> <li>• SY/TY Diploma Engineering Students</li> </ul>	
<b>Module</b>	<b>Contents</b>	<b>Hours</b>
Module-1	<b>PCB Designing Processes/Life Cycle</b> – Basics of Printed Circuit Boards, Different PCB Design/Fabrication processes, Life cycle, PCB Design CAD Tools, Selection of EAGLE	02

Module-2	<b>PCB Schematic Design with EAGLE</b> – Introduction to EAGLE, EAGLE Component libraries, loading/connecting components, tracks & netlist, Checking with ERC	04
Module-3	<b>PCB Board Design with EAGLE</b> – EAGLE Board creation, placement of components, Layout, Checking with DRC, Routing – Auto-Router, Printing PCB CAM Jobs, Preparing for fabrication	05
Module-4	<b>PCB Fabrication &amp; Population</b> – PCB Fabrication processes, photo processing, etching, drilling, Dry testing, population of the PCB with components, testing the PCB	05