



Vision of the Department:

To be recognized as a center of excellence in the field of Electronics Engineering where learners are nurtured in a scholarly environment to evolve into competent professionals to benefit society

Mission of the Department:

- Evolve a curriculum which emphasizes on strong engineering fundamentals with the flexibility to choose advanced courses of interest and gain exposure to tools and techniques in Electronics Engineering.
- Encourage a teaching-learning process in which highly competent faculty share a symbiotic association with the institutes of repute.
- Facilitate creation and dissemination of Electronics engineering knowledge through a digitally-enabled learning environment.
- Develop academic and infrastructural facilities with modern equipment and other learning resources and encourage reciprocal sharing with other institutes through networking.
- Establish a center of excellence to enhance academia – Electronics industry partnership and work on collaborative projects for benefit of society.

Programme Educational Objectives

PEO1. To enable the students to apply Electronics Engineering knowledge to design technically sound systems, adapt to new technologies through lifelong learning and excel in their career

PEO2. To inculcate research and development ability and enable the students to analyze real life problems in diverse domains to become entrepreneurs

PEO3. To make the students understand human, social, ethical and environmental context of their profession and contribute positively to the needs of individuals and society

Dr. Arun Chavan

HEAD OF DEPARTMENT, ELECTRONICS AND COMPUTER SCIENCE

Hi Everyone,
Hello Everyone,

Our department remains committed to fostering an environment of collaboration, creativity, and critical thinking. In the coming months, we look forward to further strengthening our commitment to high-quality education, research, and industry engagement.

As we begin a new semester, I warmly welcome all our students, faculty, and staff. This is a time of fresh opportunities and renewed enthusiasm, and I am excited to embark on this journey of learning and growth with all of you. The upcoming semester promises to be filled with new challenges, innovative projects, and inspiring academic endeavors. Let us continue to strive for excellence, embrace collaboration, and push the boundaries of knowledge in the fields of Electronics and Computer Science Engineering. Together, we will achieve great milestones and make this semester one to remember.

Wishing you all a productive and successful semester ahead!

‘Embrace challenges as opportunities, and let your curiosity and determination guide you towards success this semester’

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Department Updates



“ In electronics, everything is connected one way or another.” – Harry Harrison ”

ECSA Activities

Protosphere – A Hands-On Workshop on Proteus



The event was conducted by Prof. Manoj Suryawanshi and Mr. Shivam Padwal on 16th October 2024.

Objectives

- Introduction to PCB Design: To introduce participants to Printed Circuit Board (PCB) design fundamentals, including essential components and layout techniques.
- Proficiency in Proteus Software: This course will help participants master the use of Proteus for circuit design, focusing on its tools and features for creating accurate simulations.
- Practical PCB Design Skills: To teach participants how to design professional-grade PCBs, moving from schematic capture to layout and simulation.
- Hands-On Experience: To provide a practical, hands-on experience with PCB design, allowing participants to create and test their circuits in Proteus.
- Enhanced Design Skills: To enhance participants' design skills for electronics and embedded systems, making them proficient in using Proteus for future projects.

Podcast



The event was conducted by Dr. Arun Chavan on 5th September 2024.

Objectives

- To share inspiring stories of educators like Arun Chavan, highlighting their experiences and achievements.
- To discuss innovative approaches to education, such as specialized tracks for first-year students and NEP 2020 policy implementation.
- To explore the role of AI in education, emphasizing the importance of human brain and fundamental knowledge.
- To provide guidance and advice to students, encouraging participation in extracurricular activities and overcoming stage fear.
- To showcase the journey and milestones of institutions like VIT, celebrating their achievements and sharing their vision for the future.

“ As engineers, we were going to be in a position to change the world – not just study it.”
—Henry Petroski, ”

Student's Speak

Nachammai Vanitha Chettiar.

SE EXCS



Greetings Everyone, this is Nachammai Vanitha Chettiar. I'm currently pursuing my second year in Electronics and Computer Science at Vidyalankar Institute of Technology (VIT), and I'm thrilled to share my incredible academic journey so far. VIT has been a game-changer in my life, and I'm grateful for the unwavering support, innovative teaching methods, and dynamic learning environment that foster my holistic growth.

Our faculty is committed to providing an exceptional learning experience, ensuring that all concepts are clear during lectures and beyond. They strike the perfect balance between theoretical foundations and real-world applications, which not only refines our academic strengths but also encourages us to explore concepts with curiosity and analytical thinking.

VIT's implementation of the National Education Policy (NEP) 2020 has been revolutionary. By integrating co-curricular activities with core academics, we're able to expand our horizons. We enjoy the freedom to choose and experiment with subjects like Indian Knowledge Systems, Technical and Business Writing, and Presentation Skills, among others.

These offerings complement our technical education, arming us with critical life skills that prepare us to be well-rounded professionals and conscientious citizens. Our institute provides us with continuous exposure to internship opportunities from industry leaders, including ISRO, IIRS, LCT EduTEch, and Toyota. These experiences help bridge the gap between classroom learning and real-world applications, equipping us with professional challenges.

Since my first semester, building mini-projects has been an incredible initiative. The time spent on these projects helps me develop teamwork, problem-solving abilities, and a practical mindset – essential qualities for engineering success.

As a second-year engineering student at VIT, I have the privilege of accessing top-notch infrastructure, from spacious classrooms to state-of-the-art labs. The advanced facilities, including PCB printing machines and 3D printers, are a game-changer, allowing me to bring my ideas to life with ease.

As a student at VIT, I'm constantly encouraged by our professors to participate in prestigious hackathons like the Smart India Hackathon, fellowship programs like the TiH IoT Chanakya Fellowship, and many more. Our faculty provides us with the best

guidance and support during these competitions, helping us navigate any challenges that come our way. What's more, our faculty mentors ensure that we stay on track with our project work by requiring us to provide regular updates. This not only helps us stay organized but also enables them to offer targeted guidance and support when we need it most.

This year, I've taken on the role of Technical Head of ECSA, our student association. It's been an incredible learning experience, teaching me valuable skills like team management, problem-solving, and event planning. Juggling academics with co-curricular activities is tough, but it's also teaching me how to prioritize tasks and manage my time effectively.

My time at VIT is showing me the impact of hands-on learning and collaborative growth. The supportive faculty, cutting-edge resources, and vibrant campus community all come together to help students reach their full potential. I'm excited to continue learning, growing, and contributing to this amazing community.

I'm grateful for the opportunities I have here at VIT, and I'm excited to see where these experiences will take me. With the support of our faculty, I'm confident that my fellow students and I will continue to excel in our academics as well as co-curriculars and make the most of our time here at VIT.

“ To give real service, you must add something which cannot be bought or measured with money..- Sir M Visvesvaraya ”

Know an Alumnus

Name: Gaurav Shailesh Bhadoria

BE Tenure: 2020-2024



Good Day Fellow VITIANs I am Gaurav, I stand before you to share my experience of my journey with Vidyalankar Institute post my 12th I was searching for colleges from my seniors & well-wishers I was strongly suggested that if you wish to pursue Core Engineering there is no better option than VIT coming from a family of 3rd generation electronics engineering I was the 5th person from my family to pursue engineering so VIT became easily one of my top choices 4 years later I can comfortably assure you given a chance to go back in time I would do it all over again.

VIT gave me the essence of engineering. It is no more of a curriculum, it's more of a mindset, to approach a problem that makes you an engineer. This mindset of looking at a problem and taking it down block by block is what I will carry for my career going ahead. I started my journey with an absolute blank slate. under the guidance of Nayana Mam, I undertook my first project and published about it. All my professors had a very keen observing eye. They observed what I was good at provided me with opportunities that would suit my inclinations and made sure that I would always play a game from a position of strength.

They taught me how to overcome challenges, how to progress things your bad at and making it to more or less another problem that can be broken down analyzed and dealt with.

VIT has given me an environment that I call an environment of organized chaos. An environment which would give me an experience of new challenges every day but from a place where I would be unable to see how these challenges were so carefully crafted to fill my knowledge gap give me new exposure and in just the amount I could handle and make the most of these opportunities. Every Competition, every paper, every project & Every Test was like a calculated beat by a sculptor over a rock to release the potential it holds.

With all these skills I have started my journey I would forever be grateful to VIT for all it offered to me. I tried to absorb the most I could I promise to make my mentors proud & I hope I was a worthy student that VIT can look back at and be satisfied. With this I realize why my well-wishers insisted on VIT. I would do the same for anyone I would care about for this is the place where I became from a curious boy asking questions to someone capable of seeking answers. With this I conclude. Jai Hind.

“ Innovation is the ability to see change as an opportunity, not a threat.” – Steve Jobs ”

Fintech Committee Activity

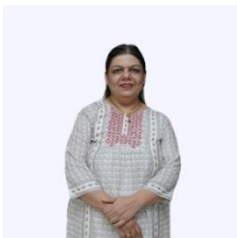


The Fintech Committee organized the event Core Meeting for the following objectives

- Provide an overview of all domains within the fintech initiative.
- Clarify the roles and responsibilities of each domain.
- Explain the process of conceptualizing, planning, and executing events.
- Foster collaboration and understanding among core members.

DEPARTMENT STAFF

Prof. ANURADHA JOSHI
ASSISTANT PROFESSOR



EDUCATION QUALIFICATION:
M.TECH

WORKING EXPERIENCE
Teaching Experience: 14 Years
Technical papers Published: 5

AREA OF SPECIALIZATION:
VLSI, Web Designing

Prof. AMOL SAKHALKAR
ASSISTANT PROFESSOR



EDUCATION QUALIFICATION:
M.E

WORKING EXPERIENCE
Industrial Experience: 34 Years

AREA OF SPECIALIZATION:
Robotics, Embedded Systems

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“ Scientists study the world as it is; engineers create the world that has never been.”
 —Theodore von Karman ”

Department updates

VIT Team competing from Bangalore has won the grand Finale of SIH 2024.

Team mentor: Prof. Nisy Mathew

Team Members:

Harshal Chavan, Ajinkya Birari, Abhay Pandey, Atharva Aurangabadkar, Saily Dhaigude, Shital Shingade
 Problem Statement Title: Development of an Educational Game on Groundwater Conservation and Management by the Ministry of Jalshakti under the software category



10 Students completed a 12-day course and Exam on "Geo-data sharing and Cyber Security" conducted by IIRS-ISRO. Prof. Akhil Masurkar coordinated the course.

We are very proud to share that our GDG Lead Ms. Maitri Dalvi (TE EXCS) and her team has secured the 1st prize at the Generative AI Hackathon organized by Aditya Birla Group and the office of Ms. Ananya Birla during Techfest 2024 at IIT Bombay. The prize money is ₹1,50,000 and her topic was "Legal AI: An AI-Powered Legal Assistant for the Indian Judiciary System". Over 400 students from top institutions across the country competed in this prestigious event.



“ Education is the most powerful weapon which you can use to change the world.” – Nelson Mandela ”

Higher Studies Committee Activity

The higher Studies Committee organized a Webinar on "Intersection of Design and Technology" on 17th October 24 by Mr. Ashish Gosavi M.Des IIT, Guwahati

VIT Vellore Institute of Technology
 Higher Studies Committee organizes
 Webinar on
“Intersection of Design and Technology”

Event Highlights

- Strategic planning for applying to master's programs in IITS
- Detailed guidance
- Tips and resources
- Profile building strategies to enhance academic and extracurricular portfolios.
- Importance of selecting and presenting relevant projects in applications.

Event Details:
Date: 17th October 2024
Time: 6:00 PM-7:00 PM
Mode: Online MS Teams

Ashish Gosavi
 M.Des IIT, Guwahati

A. M.Des at IITs

CEED 2025
 COMMON ENTRANCE EXAM FOR DESIGN

INFORMATION BROCHURE REGISTRATION PORTAL

UPDATES

The online registration for CEED 2025 will begin from tomorrow at 10:00 AM. Registration will begin for 30 days. Download the brochure for CEED 2025.

M.Des at NID (National Institute of Design)

Entrance Exam:

- NID DAT (Design Aptitude Test):
 - Conducted for admissions to M.Des programs at NID campuses.
- Exam Structure:
 - Prelims: Objective and subjective questions to assess design knowledge, problem-solving, and creativity.
 - Mains: Studio tests (hands-on tasks) and interviews for shortlisted candidates.

Eligibility:

- Bachelor's degree in Design or any related field such as Architecture, Fine Arts, or Engineering.

Process:

- Clear NID DAT Prelims & Mains.
- Studio Tests and Personal Interviews.
- Submit a Portfolio showcasing design projects and process work.

NID Campuses offering M.Des:

- NID Ahmedabad (Main campus)
- NID Gandhinagar
- NID Bengaluru

Faculty Achievements

Awarded Certificate of Appreciation for top Library users to the following Faculties
 Prof.Uma Jaishankar, Prof Amaya Pethe, Prof.Amol Sakhalkar

Dr.Sangeeta Joshi

She has become the Principal with effect from 8th October 2025.

Dr.Arun Chavan

Successfully attended Software Defined Vehicles (SDV) on 05 October 2024, organized by BVICAM, New Delhi.

Has successfully attended Evolution and Future of Next GEN Home Router on 30th November, 2024, organized by BVICAM, New Delhi.

Dr. Girish Gidaye

The International Journal of Biomedical Engineering and Technology has published a research paper. It is an ESCI journal with an Impact Factor of 0.7. Wavelet-based methodology for non-invasive detection and multiclass classification of voice disorders: a comprehensive study across multilingual datasets

The publication of a research paper in the International Journal of Biomedical Engineering and Technology (Indexed in ESCI). This work is a testament to the power of collaboration and innovation in biomedical research. Title of the Article: Speech Signals as Biomarkers: Using Glottal Features for Non-Invasive COVID-19 Testing

Dr. Sheetal Patil

Dr. Sheetal Patil has been awarded a design patent for a portable flow meter, developed in collaboration with internship students Gandhar Shidhaye, Navneet Dubey, Ayush Yadav, and Ishan Jadhav.

Prof.Uma Jaishankar

Published paper in International conference on software system and information Technology SSITCON-2024 organized by the Department of Computer Science Engineering and Department of Information Science Engineering Sri Siddhartha Institute of Technology on 18th 19th October 2024 titled "Comparative analysis of different metaheuristic algorithms in the depression detection system"

Prof. Manoj Suryawanshi

Has completed one-week STTP on "Quantum Intelligence & Generative AI: Novice to Master" from 2nd December 2024–6th December 2024 Conducted by K. J. Somaiya School of Engineering, Somaiya Vidyavihar University Mumbai

Prof.Anuradha Joshi

Published the research work on "Machine Learning Algorithms for Predicting Cardiac Arrhythmias" in Springer Nature on 24th November 2024.

Prof.Amaya Pethe

Has completed 8 week NPTEL-AICTE FDP course on System Design Through Verilog.

UPCOMING EVENTS

Fintech Committee

- IPL Auction: 10th January, 2025
- Shark Tank Edition: 20th January, 2025
- Finance Movie Screening: 7th February, 2025
- Student Loan Insights: 21st February, 2025
- Placements Talks with Campus recruiters: 7th March, 2025
- Trade-A-Thon: 21st March, 2025

IEEE Activities

January 2025

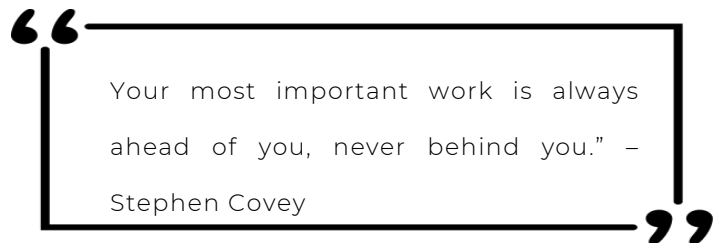
- 3D Printing Workshop
- Technical Blog Writing
- IRIS (Speaker Event) Republic Day Special on (23, 24, 27, 28)
- Tech-Web: Web-wizard

February 2025

- Software Master & Hardware Master competition in Feb (Start Round 1 First Week of Feb)
- IRIS (2nd Speaker Event)
- Voyager 2025 (Includes 3rd Speaker Event, Web-Tech & Events) Total of 5Days
- Software Master & Hardware Master competition in Feb (Final Round).

March 2025

- Software Master & Hardware Master competition in Feb (Start Round 1 First Week of Feb)
- IRIS (2nd Speaker Event)
- Voyager 2025 (Includes 3rd Speaker Event, Web-Tech & Events) Total of 5Days
- Software Master & Hardware Master competition in Feb (Final Round)



THE
EDITORIAL TEAM
PROF. AMAYA PETHE
Chief Editor