

EXAMINATION TIMETABLE (Summer: MAY- 2026)

PROGRAMME – MTech SEMESTER II (Computer Engineering) (Autonomy 2024)

Dates	Time	Course Code	Paper
04 May 2026	02:30 pm to 4:15 pm	CE65	PARALLEL ALGORITHMS AND PROGRAMMING
06 May 2026	02:30 pm to 4:15 pm	CE66	COMPUTATIONAL INTELLIGENCE
08 May 2026	02:30 pm to 4:15 pm	CE73	PROFESSIONAL ELECTIVE-3: BIG DATA
11 May 2026	02:30 pm to 4:15 pm	CE74	PROFESSIONAL ELECTIVE-4: NATURAL LANGUAGE PROCESSING
13 May 2026	02:30 pm to 4:30 pm	OE06	OPEN ELECTIVE-2- IPR AND PATENTING

PROGRAMME – MTech SEMESTER II (Electronics & Telecommunication Engineering) (Autonomy 2024)

Dates	Time	Course Code	Paper
04 May 2026	02:30 pm to 4:15 pm	ET73T	ADVANCED DIGITAL SIGNAL PROCESSING
06 May 2026	02:30 pm to 4:15 pm	ET75T	OFDM & MIMO TECHNOLOGY
08 May 2026	02:30 pm to 4:15 pm	ET81	PROFESSIONAL ELECTIVE-3: SMART SENSORS AND INTERNET OF THINGS
11 May 2026	02:30 pm to 4:15 pm	ET98T	PROFESSIONAL ELECTIVE-4: INDUSTRIAL IOT
13 May 2026	02:30 pm to 4:30 pm	OE06	OPEN ELECTIVE-2- IPR AND PATENTING

Change if any, in the timetable shall be communicated on the vRefer and Exam cell notice board.


Principal